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|---|---|---|---|---|---|
| | MaxChobi<br>P5K Deluxe<br>E8400<br>Super Talent<br>T8UA1G8C5<br>4x 1GB DDR2 800 Mhz<br>PC6400<br>Heatspreader 1G/128x8<br>CL5<br>BIOS 0809<br>16/11/08<br>22.03 | | | | |
| Configure Advance CPU Settings | | | | | |
| | | | | | |
| CPU Ratio Setting: | 09.0 | | | | |
| C1E Support: | Disabled | | | | |
| Max CPUID Value Limit: | Disabled | | | | |
| Vanderpool Technology: | Disabled | | | | |
| CPU TN Function: | Disabled | | | | |
| Execute Disable Bit: | Disabled | | | | |
| | | | | | |
| | | | | | |
| JumperFree Configuration Settings | | | | | |
| | | | | | |
| AI Overclocking: | Manual | | | | |
| CPU Ratio Setting: | 9.0 | | | | |
| FSB Strap to North Bridge | 333 Mhz | | | | |
| FSB Frequency: | 400 Mhz | | | | |
| PCI-E Frequency: | 100 | | | | |
| DRAM Frequency: | DDR2-801Mhz | | | | |
| DRAM Command Rate: | 2N | | | | |
| | | | | | |
| DRAM Timing Control: | Manual | | | | |
| Timing Information | [5-5-5-15-6-45-6-3-3] | | | | |
| CAS# Latency: | 5 Dram Clocks | | | | |
| RAS# to CAS# Delay: | 5 Dram Clocks | | | | |
| RAS# Pre Time: | 5 Dram Clocks | | | | |
| RAS# Act Time: | 15 Dram Clocks | | | | |
| RAS# TO RAS# Delay: | 6 Dram Clocks | | | | |
| REF CYCLE Time: | 45 Dram Clocks | | | | |
| Write Recovery Time: | 6 Dram Clocks | | | | |
| Write to Read Delay: | 3 Dram Clocks | | | | |
| Read to Pre Time: | 3 Dram Clocks | | | | |
| DRAM Static Read Control: | Disabled | | | | |
| | | | | | |
| Transaction Booster: | AUTO | | | | |
| | | | | | |
| CPU Voltage: | 1.3000v | | | | |
| CPU PLL Voltage: | 1.80 v | | | | |
| FSB Termination Voltage: | 1.40 v | | | | |
| DRAM Voltage: | 2.30 v | | | | |
| North Bridge Voltage: | 1.40 v | | | | |
| South Bridge Voltage: | 1.20 v | | | | |
| Clock Over Charging Voltage: | 1 v | | | | |
| Load Line Calibration: | Enable | | | | |
| CPU GTL Voltage Reference: | 0.57x | | | | |
| NB GTL Voltage Reference: | 0.61x | | | | |
| CPU Spread Spectrum: | Disabled | | | | |
| PCIE Spread Spectrum: | Disabled | | | | |
| | | | | | |
| | | | | | |
| North Bridge Chipset Configuration | | | | | |
| | | | | | |
| Memory Remap Feature: | Disabled | | | | |
| Initiate Graphic Adapter: | PEG/PCI | | | | |
| PEG Port Control: | Auto | | | | |
| PEG Port Force x1: | Disabled | | | | |
| ASUS C.G.I Function: | Disabled | | | | |
| | | | | | |

| Memorias | CL | RCD | RP | CAS | RC | RFC | RRD | WR | WTR | RTP |
|---|---|---|---|---|---|---|---|---|---|---|
| Supertalent 1 @ 400 Mhz | 5 | 5 | 5 | 18 | 23 | 51 | 3 | 6 | 3 | 3 |
| Supertalent 1 @ 266 Mhz | 4 | 4 | 4 | 12 | 16 | 34 | 2 | 4 | 2 | 2 |
| Supertalent 1 @ 200 Mhz | 3 | 3 | 3 | 9 | 12 | 26 | 2 | 3 | 2 | 2 |
| | | | | | | | | | | |
| Supertalent 2 @ 400 Mhz | 5 | 5 | 5 | 15 | 21 | 42 | 3 | 6 | 3 | 3 |
| Supertalent 2 @ 266 Mhz | 4 | 4 | 4 | 10 | 14 | 28 | 2 | 4 | 2 | 2 |
| Supertalent 2 @ 200 Mhz | 3 | 3 | 3 | 8 | 11 | 21 | 2 | 3 | 2 | 2 |

DIMM1- Supertalent 2
DIMM2- Supertalent 1
DIMM3- Supertalent 2
DIMM4- Supertalent 1